

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Muir

Art Unit : Unknown

Serial No. : 09/992,554

Examiner : Unknown

Filed : November 6, 2001

Title : DIODE DISCOVERY POWER LEVEL DETECTION

COPY**Attention: Official Draftsman**

Commissioner for Patents

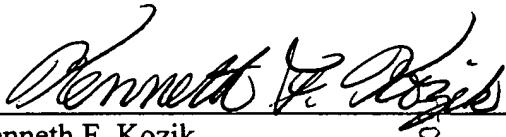
Washington, D.C. 20231

TRANSMITTAL OF FORMAL DRAWINGS

In response to the Notice to File Missing Parts dated December 6, 2001 enclosed please find (5) sheets of formal drawings for the corresponding drawings presently in the application. Please note that a Response to Notice to File Missing Parts with a signed Combined Declaration and Power of Attorney has already been filed on February 6, 2002 (full copy enclosed). Accompanying the attached drawings is a Petition for One-Month Extension of Time and a check for \$110.00.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: March 6, 2002
Kenneth F. Kozik
Reg. No. 36,572

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225 Franklin Street
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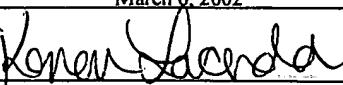
CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

March 6, 2002

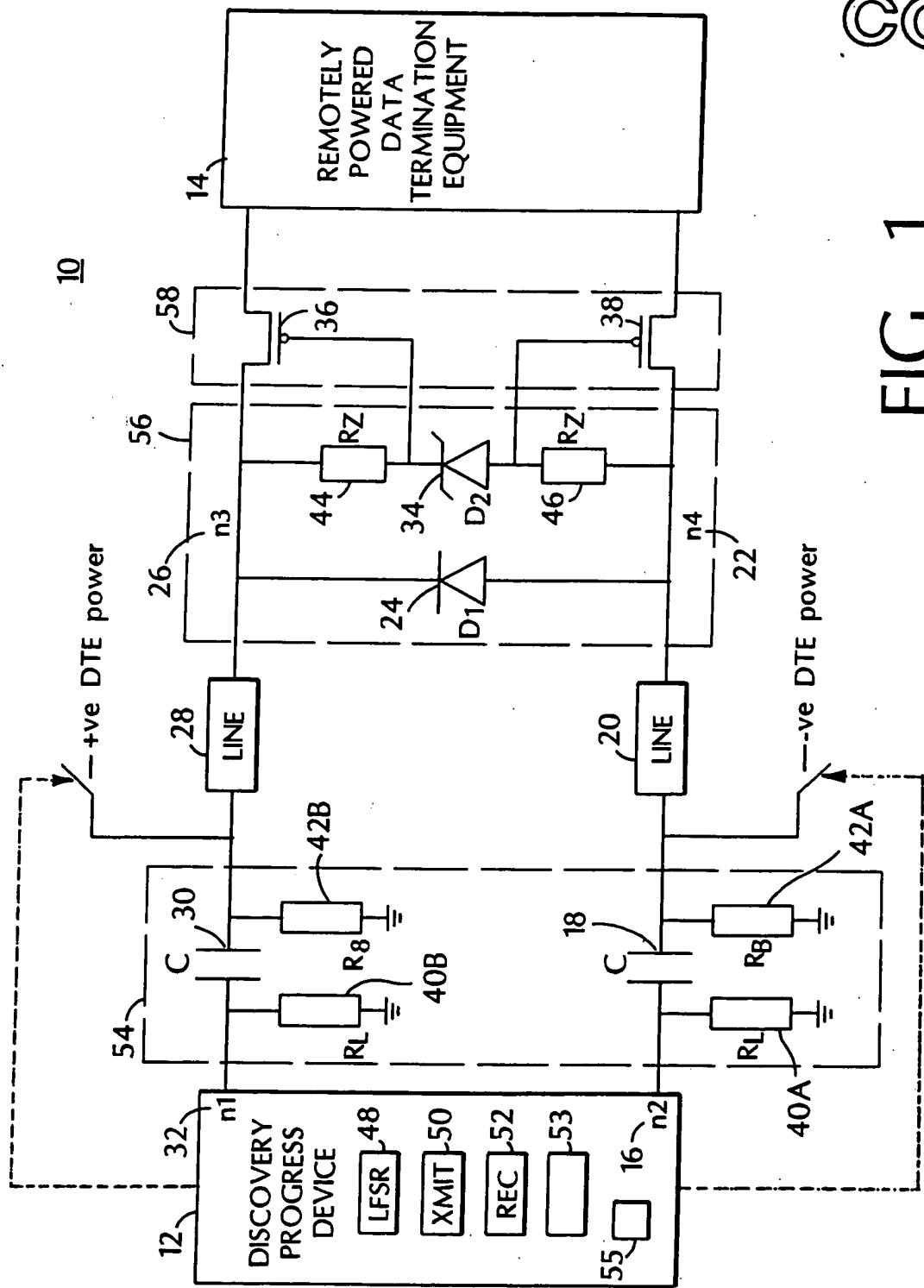
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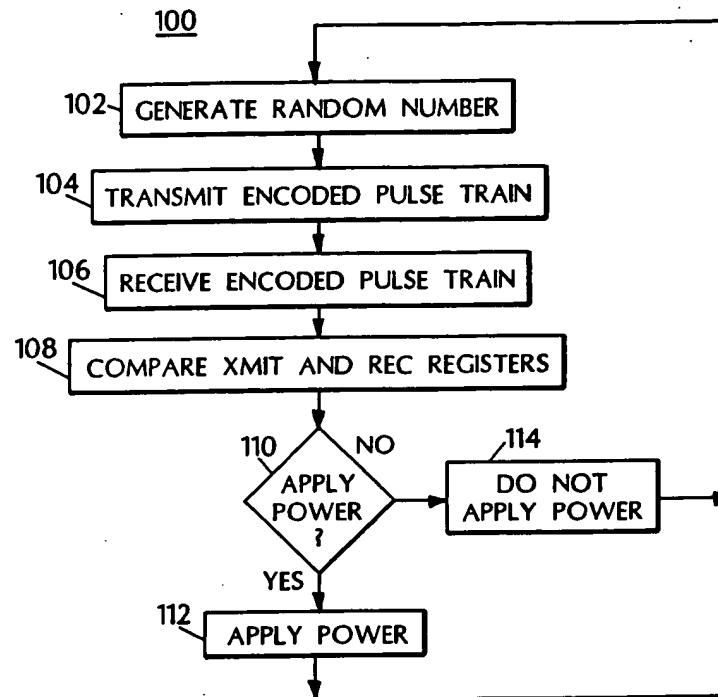


Karen B. Lacerda

Typed or Printed Name of Person Signing Certificate



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EIG



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FIG. 2

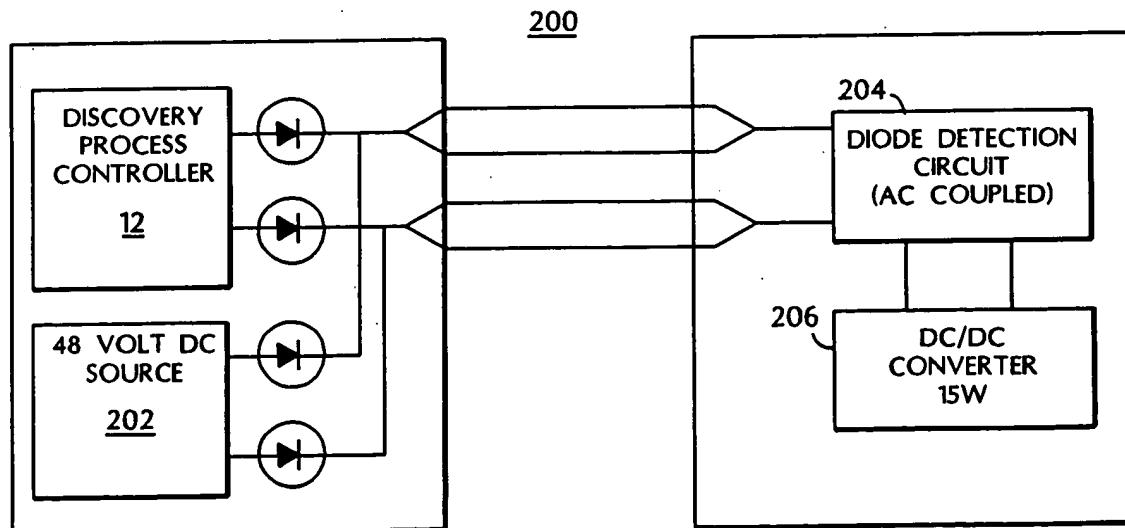


FIG. 5

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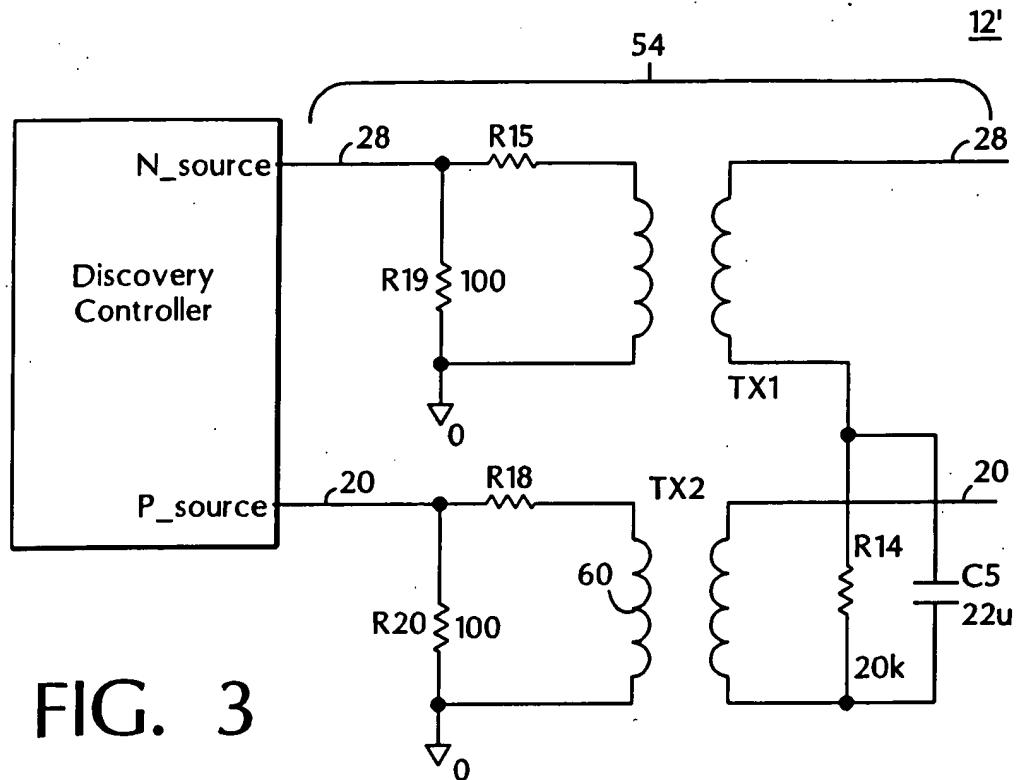


FIG. 3

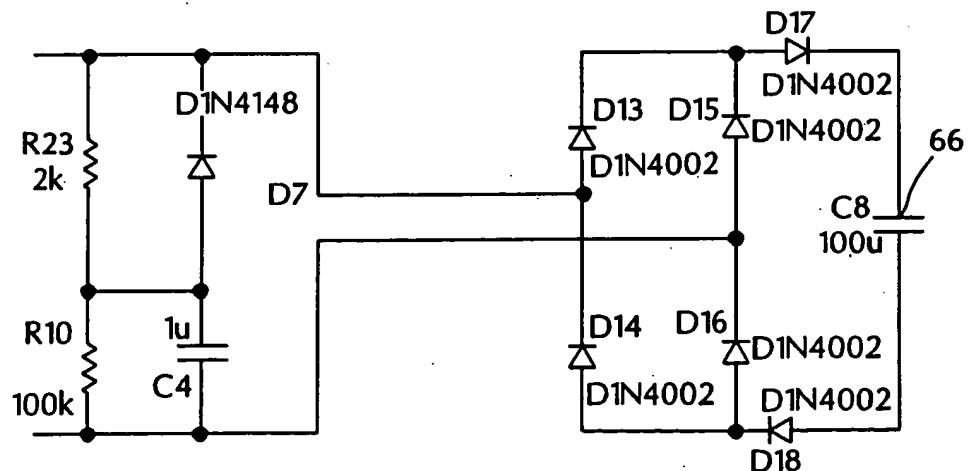


FIG. 4

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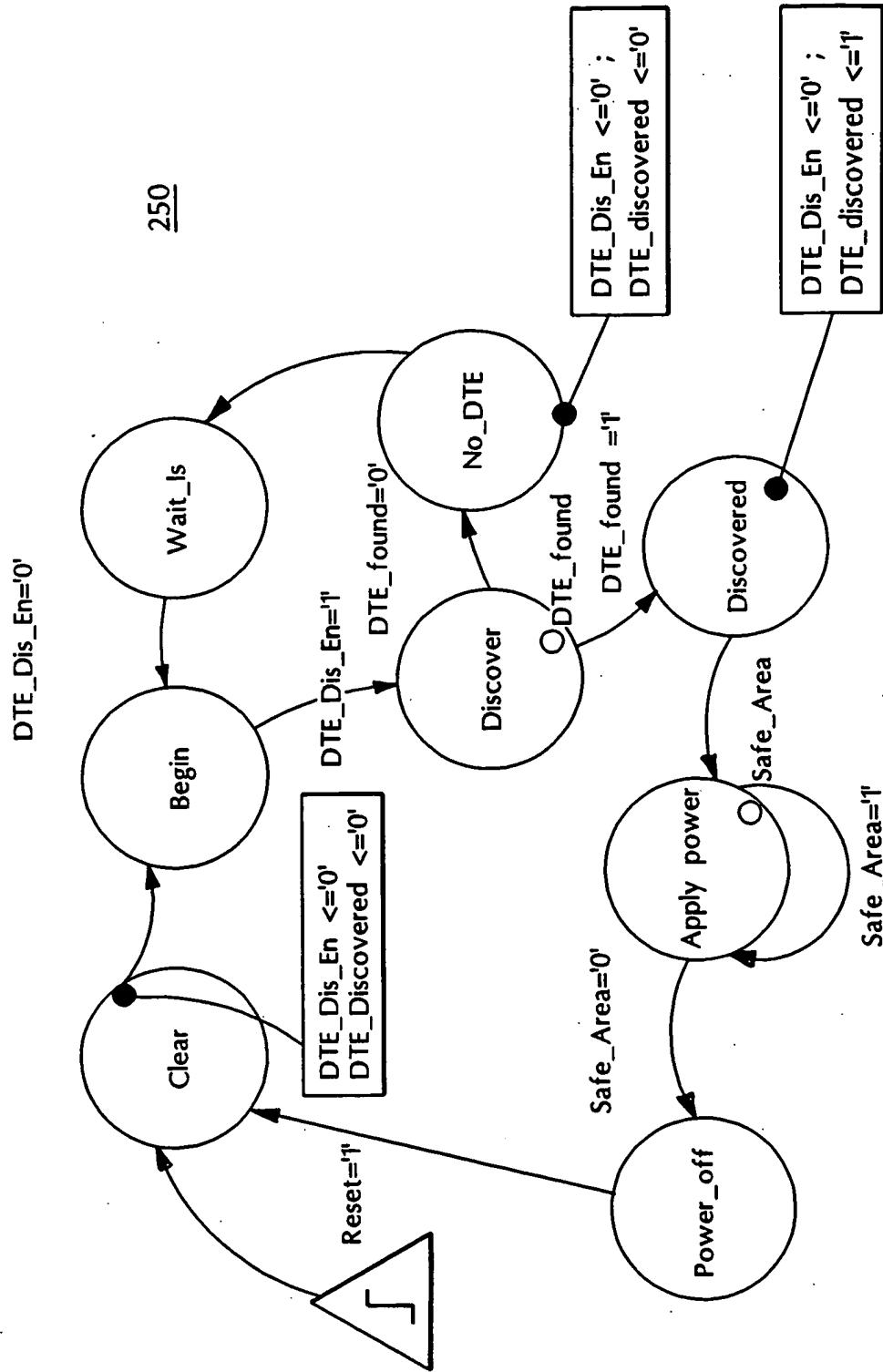


FIG. 6

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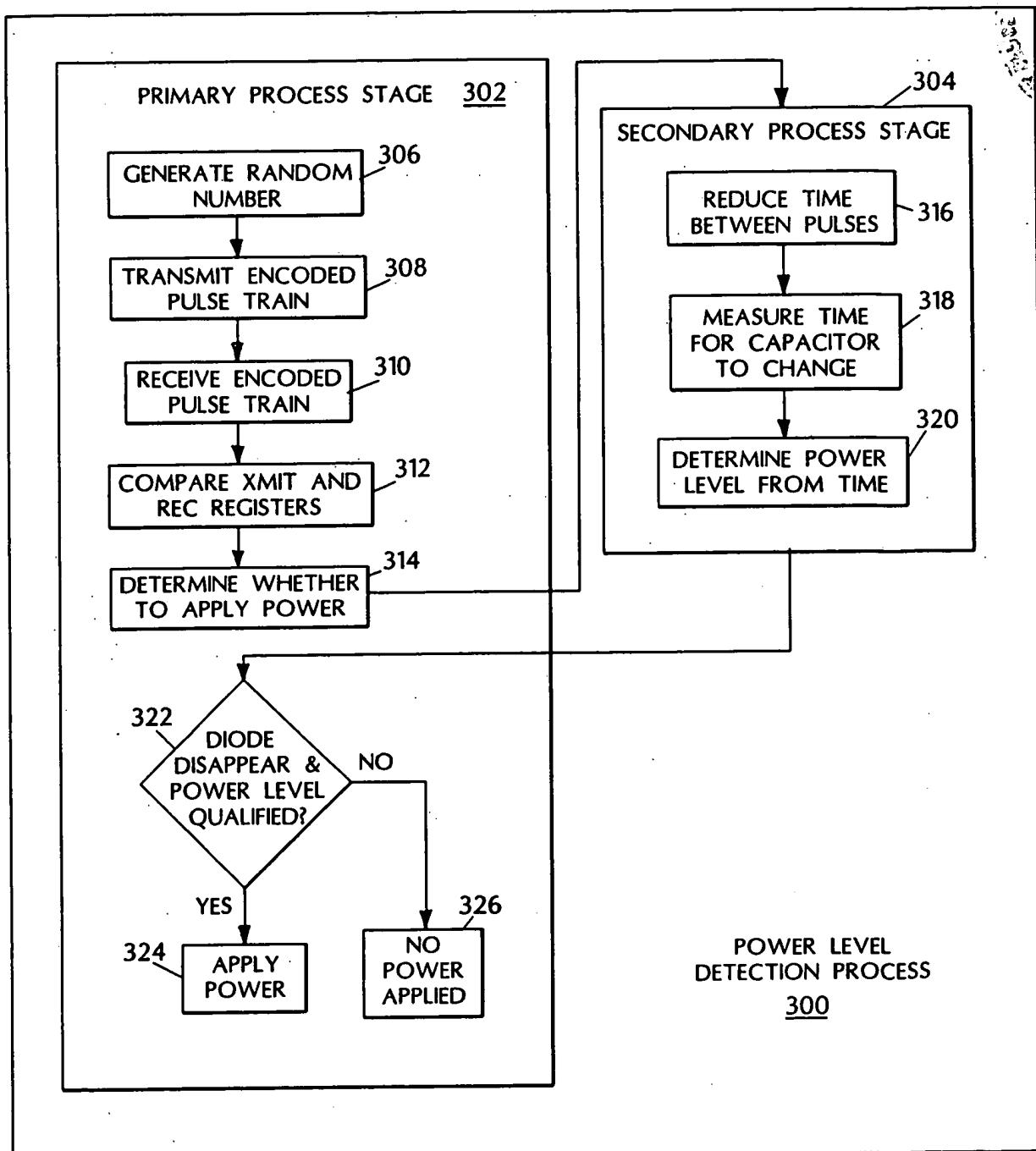


FIG. 7